



MIT S
MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE
(Deemed to be University under section 3 of UGC Act, 1956)

EXAMINATION SECTION

ACADEMIC YEAR: 2025-26

Time Table - M.Tech. I Year II Semester - II Mid Term Test -June 2026

TIMINGS: FN: 11:00 A.M to 01:00 PM

AN: 03:00 P.M to 05:00 PM

Date/Day	Session	Computer Science and Engineering (CSE)	VLSI Design and Embedded Systems (VES)
08.06.2026 (Monday)	FN	Advanced Machine Learning-25MBCSETC03	CMOS Analog IC Design-25MBVESTC03
	AN	Advanced Data Visualization Techniques- 25MBCSETC04	Embedded System Design-25MBVESTC04
09.06.2026 (Tuesday)	FN	Cyber Security-25MBCSEDC09	Pattern Recognition and Machine Learning- 25MBVESDC07
	AN	Responsible AI-25MBCSEDC10	High Speed VLSI Design-25MBVESDC12

Note 1: In case of students who have opted a course/s through MOOCS they may ignore Mid-term Test for all such courses

Dated: 21.05.2026

CONTROLLER OF EXAMINATIONS

To be read in all the M. Tech-(CSE, VES) I Year II Semester Classrooms

CONTROLLER OF EXAMINATIONS
Madanapalle Institute of Technology & Science
(Deemed to be University)
MADANAPALLE - 517 325, A. P.

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Note 2: HoD is requested to circulate among the faculty members concerned